

GUEST LECTURE

on

DIGITAL

INTEGRATED- CHIP

DESIGN

by

Dr. G. Anantha Rao

Associate Professor

GMRIT

27-04-2023



AVANTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY

Department

Of

Electronics and communication Engineering (ECE)

Email-id:avanthiq7.ecehod@gmail.com

Electronics and Communication Engineering (ECE)

CONTROL SYSTEMS GUEST LECTURE

Consolidate Report

S.No	Department	Total Students	Total Participants
1	ECE	125	117
	Total	125	117

SIGNATURE OF HOD



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Speaker: Dr.G. Anantha Rao

Designation: Associate Professor

Institution: GMRIT



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ON
DIGITAL INTEGRATED-
CHIP DESIGN

Resource Person

Dr. G. ANANTHA RAO

Associate Professor

GMRIT

Date:

18-03-2023

Saturday



AVANTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY

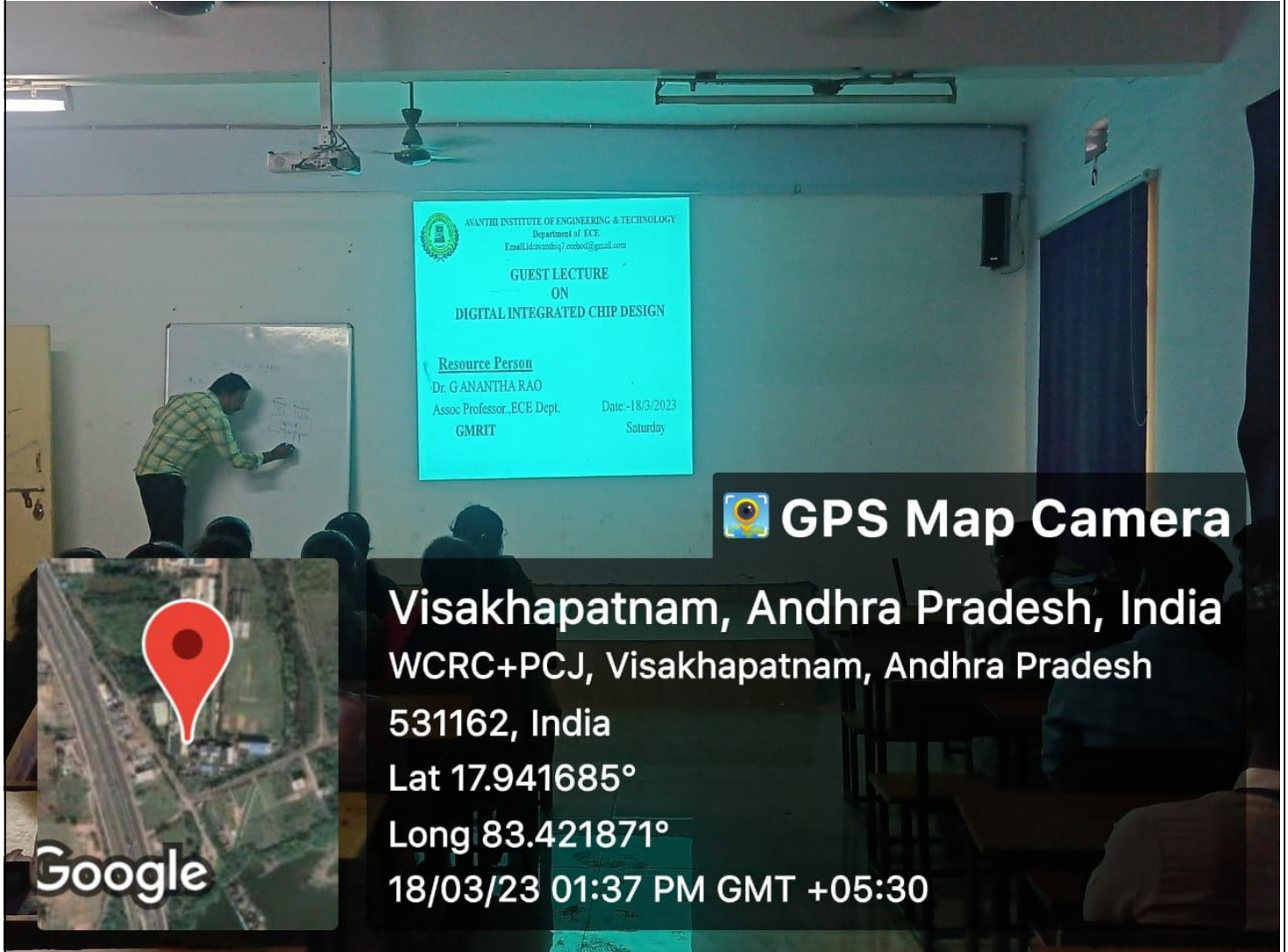
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Some pictures:





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